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Fig. 2(A) is a top plan view of the pixel unit of the active matrix substrate, while Fig. 2(B) is a connection diaphragm of the circuit arrangement of the pixel unit. In actuality, the pixel unit (image display unit) is so constructed that a plurality of pixels are arrayed in the shape of a matrix. Incidentally, a sectional view taken along A-A' in Fig. 2(A) corresponds to the sectional view of the pixel unit in Fig. 1. Accordingly, common reference numbers are indicated in Fig. 1 and Fig. 2(A), both of which may be referred to on occasion. Besides, two pixels are illustrated in the top plan view of Fig. 2(A), and they have the same structure. As shown in Fig. 2(B), two FETs per pixel are disposed for the organic EL element 205. Both the FETs are of n-channel type, and they function as the switching FET 203 and the current controlling FET 204. Reference numeral 149 designates a gate wiring.

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The active matrix substrate formed with the organic EL layer is sealed in a package in order to be cut off from external shocks, and ambient conditions such as dust and humidity. The shape and scheme of the package are exemplified in Fig. 4. A bed plate 401 is formed of an insulating material such as ceramics, and the active matrix substrate 413 formed with the organic EL layer is fixed thereon by a low-melting glass or metallized layer 402. The active matrix substrate 413 is connected with an external circuit by a lead frame 403, which is connected with the active matrix substrate 413 by wire pieces 412 of gold (Au) through pads 410 for wire bonding.

IN THE CLAIMS:

Please amend claims 1, 3, 5, 7, 9, and 11 as follows:

1. (Amended) An active matrix type organic EL display device comprising:
an insulating gate field effect transistor provided on a single crystal semiconductor substrate;